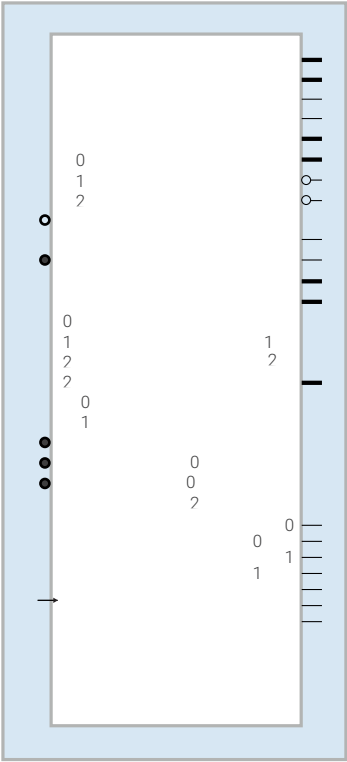
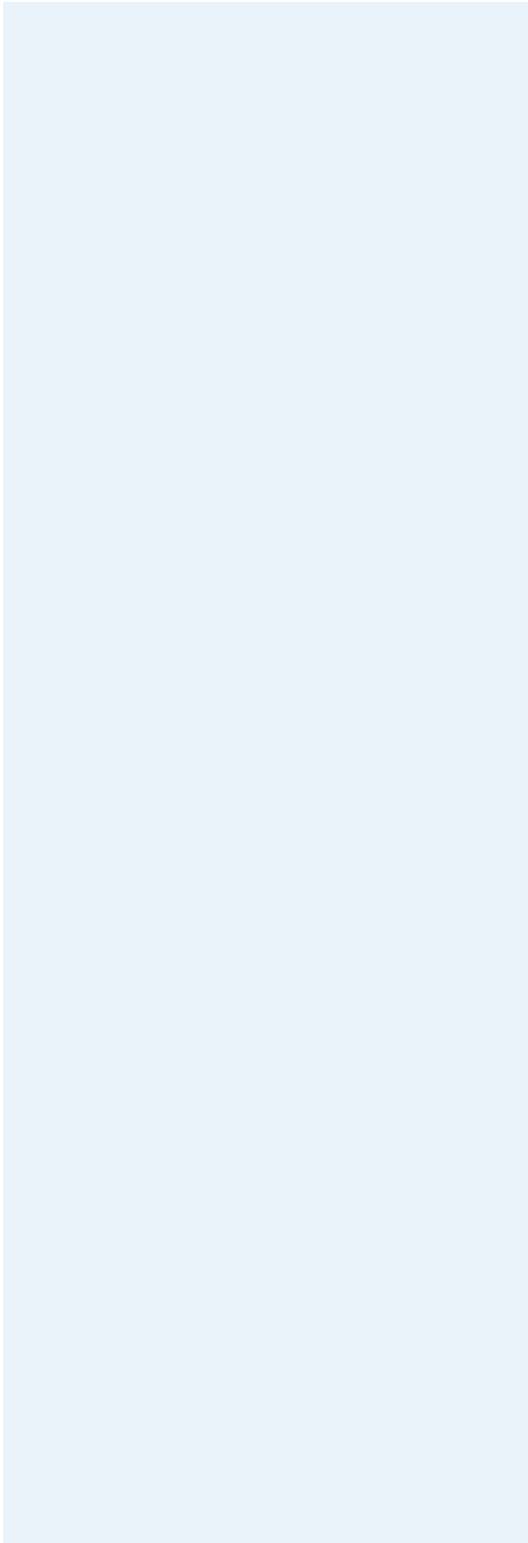




Overview

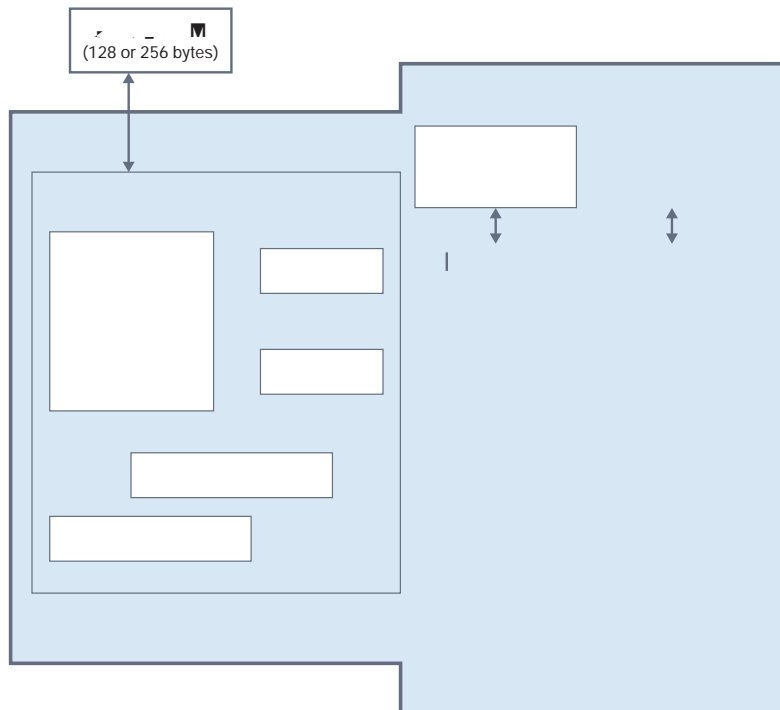


Proven Quality, Complete Solution

803x/805x Compatibility

High-Performance Architecture

DW8051 Configurable Architecture



					0 320	051
	031	051	0 32	0 52		
Clocks Per Instruction Cycle	12	12	12	12	4	4
Internal ROM (1)	—	4KB	—	8KB	—	Up to 64KB
Internal RAM (1)	128 bytes	128 bytes	256 bytes	256 bytes	256 bytes	128 bytes or 256 bytes
Data Pointers	1	1	1	1	2	2
Serial Ports	1	1	1	1	2	2,1, 25te

s1

