Portable Stimulus from IP to SoC CoStart

VELOCITY
Accelerate Your Verification

Accelerate verification with Synopsys tools and methodology experts

Overview

As products become more complex and market windows shrink, we need to understand what improves efficiency in verification and how can these improvements be achieved? The Synopsys Portable Stimulus Solution enables verification stimulus to be captured, automatically generated, and for the same specification to be reused. Expert guidance and consultation from Synopsys' Portable Stimulus Solution methodology experts can help create and optimize the best workflow for customer's specific needs and requirements. The Synopsys Portable Stimulus Solution provides you with the recommended steps to architect your Portable Stimulus Solution environment.

About Portable Stimulus CoStart for IP to SoC Verification Management

Portable Stimulus CoStart enables customers to accelerate coverage convergence by achieving coverage 10-100X faster than constrained random stimulus, and provide solutions that enable productive verification at the Block, Subsystem, and SoC levels. Customers can reuse SystemVerilog constraints & coverage with portable stimulus and integrate it into block and SoC level environments easily.

Portable Stimulus CoStart is a 10-day service where Synopsys experts engage with the customer's verification team to understand the existing verification methodology requirements, helping to describe the system functionality at a high-level using resources and constraints, creating solution/coverage, and mapping the same generated solution to different platforms such as virtual, simulation, emulation and silicon. To complete the services engagement, Synopsys will provide hands-on training for up to five customer engineers on how-to run the tool, as well as debugging and interpreting the results.