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The Synopsys SLM SMS IP solution is a comprehensive, integrated test, repair and diagnostics solution that supports repairable or nonrepairable embedded memories across any foundry, process node or memory IP vendor. Silicon-proven in over a billion chips on a range of process nodes, the Synopsys SLM SMS IP is a cost-effective solution for improving test quality and repair of manufacturing faults found in advanced test nodes or code of conduct (en-US)/MCID 5DC 2021

Tester Patterns and Diagnostics

The Synopsys SLM SMS IP Yield Accelerator addresses the need to identify, analyze, isolate, and classify memory faults as designs are readied for transition from first silicon to volume manufacturing rapidly, cost-effectively and accurately. Leveraging the infrastructure of the Synopsys SLM SMS IP, the Yield Accelerator automatically generates vectors for test equipment and provides fault analysis and root-cause failure guidance based on silicon test results. Using this feature, test and product engineers can rapidly analyze failures manifested in embedded memories and inspect the physical location and class of each fault to determine the root cause in the manufacturing and development of designs.