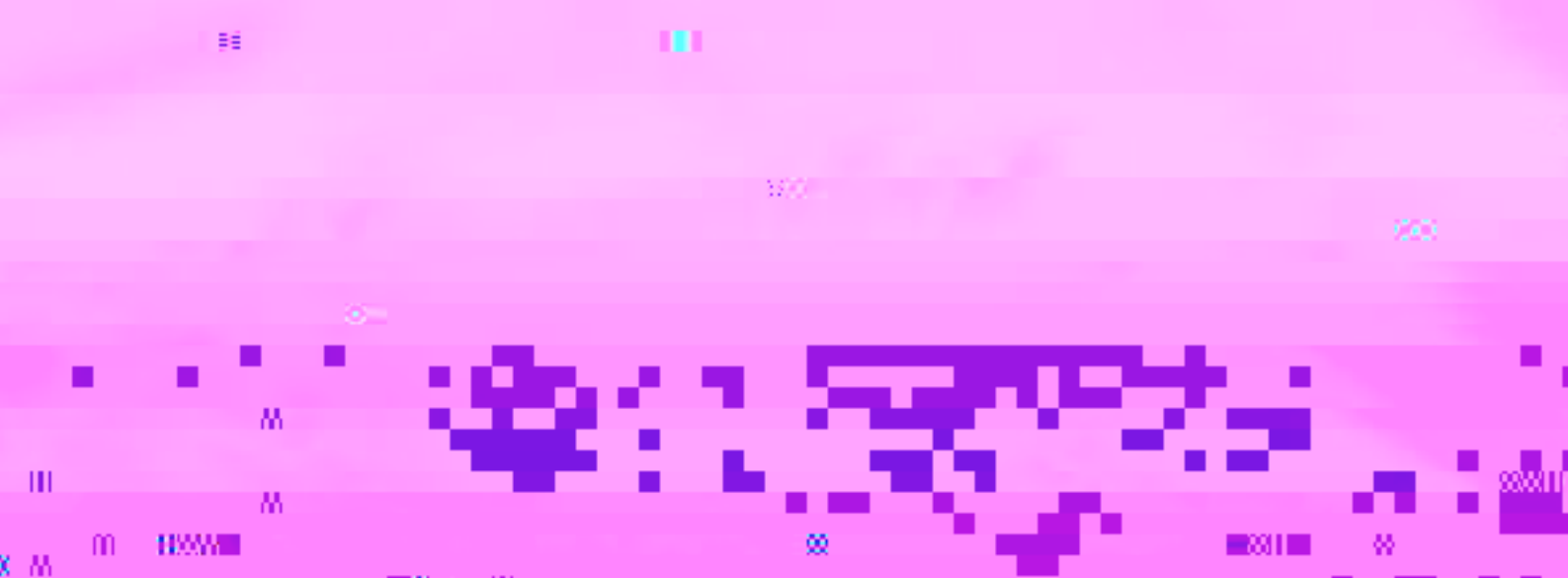SYNOPSYS®

SYNOPSYS CLOUD CHIP DESIGN PLATFORM USER EXPERIENCE SURVEY RESULTS

AN INFOSYS CONSULTING ADOPTION AND BENEFITS STUDY

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Synopsys is a leading provider of advanced tools for semiconductor chip design, verification, IP integration, and application security. Synopsys Cloud with FlexEDA combines the availability of advanced compute and storage infrastructure with unlimited access to EDA software licenses on-demand. Customers can mix and match between two deployments – Bring Your Own Cloud (BYOC) and Software-as-a-Service (SaaS) – to best address their evolving requirements. Synopsys Cloud BYOC with FlexEDA supports the AWS, Microsoft Azure, and Google Cloud platforms. The SaaS offering leverages the Microsoft Azure cloud platform for all infrastructure resources.

Synopsys wanted to conduct a time-to-market benefit study among its current customers who have adopted the Synopsys Cloud platform with the FlexEDA business model to better understand their customers' experience.

This study focused on uncovering and understanding customer feedback and experiences using Synopsys Cloud. We analyzed their views on adoption, motivation, objectives and goals, and quantitative and qualitative benefits derived from Synopsys Cloud, focusing on efficiency, productivity, and operating agility.

Synopsys Cloud SaaS offers built-in EDA tools backed by tailored compute resources that allow customers to customize their environments to meet their chip design requirements. The customers that we interviewed

- One of the key drivers for implementing a Synopsys Cloud SaaS solution is related to the flexibility of access to unlimited licenses and the ability to ramp up and ramp down licensing as well as compute resources. Peak demands required the addition of compute power and licenses as needed, which was enabled by Synopsys through their FlexEDA pay-per-use (PPU) licensing model.

- Having to hire technical experts, procure, and set up environments from scratch—whether on-premises or on other cloud platforms—led the customers to choose a SaaS solution. The ready-to-go nature of the Synopsys Cloud SaaS platform, which is specifically geared towards chip design, allowed customers to avoid high set-up costs.

- Many customers we interviewed reported experiencing performance disruptions in their traditional environments, as well as bugs in tools and configuration stages. One of the main draws of Synopsys Cloud SaaS was its stable and high-performing environment that offers considerable compute power and established, state-of-the-art tools that deliver a seamless chip design flow.

- The customers we spoke to wanted to focus on their design projects rather than on the administration of infrastructure and licenses, which formed a key motivator in choosing Synopsys Cloud SaaS. The ability of the platform to scale and deploy license servers automatically with instant license key activation offered significant value in reducing design overheads.

- Overall, E DA runtime improved by 40% compared to traditional deployment due to scalability of licenses and compute.

Customers identified a few areas of improvement to enhance the experience on the platform. These included having more flexibility with metered PPU licensing model to enable granular measurement of shorter distributed jobs, better representation of usage data on the analytics dashboard with expanded user-configurable options and real-time refresh of analytics charts and data.

Synopsys Cloud SaaS enables design optimization, effective device and process simulations, and timely access to the most up-to-date IP libraries — all of which work to increase productivity. Developers now have more time for verification and testing, leading to first time-right designs and greater adherence to design-for-manufacturing principles. These benefits ultimately allow faster time-to-market and enable customers to capture greater market share.

CONCL ↓ION

The TTM benefit study of Synopsys Cloud considers the quantitative and qualitative benefits that customers interviewed realized over the usage period. These cus² e

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